

Journey of Three Decades

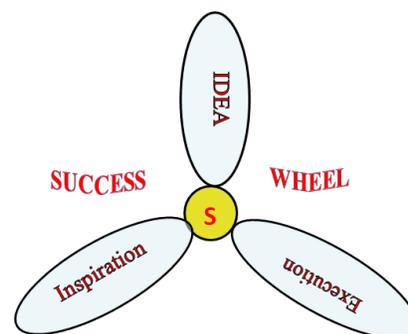
Dr. Sunil N. Kore



Philosophy: There is no dream you cannot reach. There is no goal you cannot grasp. There is no success you cannot embrace. Be sure strength is within you to create destiny. Arise, Awake and not stop till goal is reach

Ideology

- Continuous Improvement in the system by involving all those who are the part of system.
- Motivate the peoples for realistic self-assessment, identifying the strength and weakness of my organization or department, look into the opportunities and possible threats and contribute to a system so that it will become more dynamics.
- Encourage the staff to adopt new skills to impart distilled knowledge, inculcate a sense of discipline and social values among students, to become capable engineer or person.
- Amalgamate the academics and need based research to strengthen Industry-Institute interaction and partnership.
- Create sense of belonging among stakeholders towards organization.
- Belief on change is the only constant in the nature. Constant efforts to achieve academic excellence, development of state of art laboratories and infrastructure to surpass the challenges of global competition.



Name: Dr. Sunil Nilkanth Kore . Mobile :09970175105

Post: Associate Professor in Electronics

- Qualification:**
- ❖ Diploma in Industrial Electronics (Walchand College of Engineering) 1980
 - ❖ B.E. (E & TC) (College of Engineering Pune) 1983
 - ❖ M.E (Electronics) (Walchand College of Engineering) 1991
 - ❖ PhD (Electronics) Shivaji University 2017
 - ❖ GATE Qualified
- Award & Achievements**
- ❖ Late Padmashree Annasaheb Behare Best Teacher Award from Indira Behare Charitable Trust, Pune.
 - ❖ NBA Accreditation for Electronics Department – **5 years**
 - ❖ SWOT and Training Need Analysis for Govt. and Govt. Aided Institutions
- Work Experience: (Industrial)**
- ❖ Technofour Pune (R & D Engineer)
 - ❖ Tata Institute of Fundamental Research Bombay (Scientific Officer)
 - ❖ Elconix (Entrepreneur : Mfg. of Educational Kits , Microprocessor & microcontroller Kits, Induction motor & Air Conditioner Protection Unit)
- Work Experience: (Teaching)**
- ❖ 4 Years as Lecturer
 - ❖ 25 years as Associate Professor at Walchand College of Engineering.
- International Experience:**
- ❖ International Interactive Workshop on Effective Academia Leadership Strategies SEGI University, Kuala Lumpur, Malaysia 12-14 December, 2013
- Positions Held:**
- ❖ Head of Department (Information Technology)
 - ❖ Head of Department (Electronics)
 - ❖ Head Central Computing Facility
 - ❖ Dean Faculty
 - ❖ Dean Academics
 - ❖ Remote Centre Coordinator (NMEICT- MHRD Project by IIT Mumbai & Kharagpur)
 - ❖ Coordinator for VISION Activity (National Level Technical Exhibition)
 - ❖ BoS member in Shivaji University
 - ❖ BoS member in Solapur University
 - ❖ DAB member in D.Y. Patil college of Engineering Kolhapur
 - ❖ DAB member in Govt. college of Engineering Karad
 - ❖ DAB member in SEVARI's college of Engineering Phandarpur
 - ❖ Purchase Committee Member
- Publications:**
- ❖ International Journal papers– 8
 - ❖ International Conference Papers– 8
 - ❖ National Conference papers - 6
- Workshops**
- ❖ Conducted – 25 & Attended - 40
- Professional Membership and Committee members**
- ❖ ISTE Society Member
 - ❖ IET Member
 - ❖ LIC Committee member for Shivaji & Solapur University
 - ❖ Subject Expert for Faculty selection in Shivaji University
 - ❖ Inspection Committee member for Engineering Colleges from AICTE and DTE.
- Consultancy, Grants and IRG Generation:**
- ❖ AICTE R & D Project worth of Rs. 5 Lacks
 - ❖ Resource Generation through Remote Centre worth of Rs. 2 lacks
 - ❖ Donations and discount in form of material worth of 60 Lacks.

Special Contribution

Institutional Infrastructural Developments

- ❖ An Entire institute Fiber Optic Backbone with 160 nodes Central Computing Facility were planned and successfully implemented. A Serves for E-learning, Video Streaming and Conferencing, Email were configured with hotspot Wi-Fi Network. A state of art of modular Furniture, False Ceiling and on line UPS systems were designed in house. A Projects was worth of 2.5 cr.
- ❖ Establishing Moodle Server for Online attendance , Assessment, learning Management System
- ❖ Development of Information Technology Department laboratories such as multimedia, Networking, Operating Systems (Linux, Windows), Digital electronics etc. Online attendance, assessment, feedback system software.
- ❖ Development of Department laboratories such as Electronic Design Automation, Digital signal processing, Communication Engineering, VLSI and Embedded system lab etc.

Institutional Curriculum and Faculty Development

- ❖ As a Dean Academics entire curriculum structure development for all branches with inclusion of Industrial/ Institutional (IIT,DRDO etc), Sponsored Project internship for UG (Six months) and PG (one year). Looking into the curriculum of IIT, BITS, MIT USA, Stanford USA and AICTE model Curriculum a choice based credit system were developed at WCE.
- ❖ Development of own ideal Autonomous Institute Model for best performance and Formation of Rules and Regulations.
- ❖ A SWOT analysis and Training Need Analysis is done for Govt. and Govt. Aided Institutions (COEP Pune, SP College Mumbai, COE Chandrapur, Jalgoan, Aurangabad, SGGGS Nanded, WCE Sangli). A systematic way was adopted to get online feedback and personal interaction from stakeholders (Board of Governance, Director/Principal, HoD's , Faculty, Placement officer, parents, Students, students organization, LMC , Alumni, Industry etc.) for entire academics teaching , learning process, delivery, outcome, amenities, extracurricular, co-curricular activities, placement, industry – Institute interaction, Institute –Institute interaction , Research Activities and patents , IT and web based infrastructure etc were considered for formation of SWOT report and GAP analysis. A report along with the recommendations were submitted for improvement in teaching and learning processes , students facilities improvement and faculty and support staff skill development and training programs.
- ❖ Excel Sheet programs were developed for load distribution, faculty requirements, CO-PO attainment and analysis for autonomous institute.
- ❖ As a faculty some courses were designed for faculty developments
 1. “Microprocessor Peripherals & interfacing “(One Year)
 2. “Digital Design and VHDL” (10 Days)
 3. One-week On-line training program on “Proteus simulation software”
 4. “Design and Implementation of Fixed Voltage Power Supply”
 5. Training program on “MULTISIM” software
 6. A five day training program on “Red-Hat Linux”
 7. Four Day Workshop on “Fundamentals of DSP” “Training in DSP and Embedded C” By Dr. Ganesh Bhokare, Silicon Machine, Pune
 8. “Embedded C for DSP and Image Processing” By Dr. Ganesh Bhokare, Silicon Machine, Pune
 9. Around 20 Programs were conducted remotely by IIT Mumbai and Kharagpur on various topics NMEICT – MHRD Programs.
 10. Curricula development with Dr. Louney Morel USA

Industry Interaction: Projects Completed in Following Industries

1. Adept Fluidyne Pune
2. Shreya System Pune
3. FIE Foundation, Ichalkaranji.
4. Mahindra and Mahindra Bombay
5. Shreem Capacitors Jaysingpur
6. Power Precision Control Pune.
7. TCS Pune.
8. Hella Pune.
9. Simens Mumbai
10. LinkedLoops Pune

Guest Lecturers at Institutions

1. SEVARI'S College Of Engineering Phandharpur.
2. D. Y. Patil College of Engineering Kolhapur
3. KIT College of Engineering Kolhapur
4. KBP Satara
5. VIT Pune
6. VIIT Pune
7. PVPIT Bhudhgoan
8. J.J. Magdum Jaysinpur
9. Willigdon College Sangli.
10. COE Karad

Area Of specialization

1. Digital Design
2. Microprocessor, Microcontroller , Embedded Systems
3. Signal & System
4. Digital Signal Processing
5. Probability, statistics and Random Processes
6. Operational Amplifiers & Circuit Design
7. Electronic System Design & Manufacturing
8. Computer Networking and Packet Switching
9. Design analysis of algorithm
10. Basic Electronics

Publications: International Journal

Sr.No.	Title of the paper	Authors	Name of the Conference
1	Image Retrieval Techniques Based on Image Features : A State of Art Approach for CBIR	Mr. Kore S. N. Mr. Kolkure V.S. Mr. Kondekar V.H.	International Journal of Comp. Sci and Info. Security (IJCSIS) Vol 7, No.1, 2010. ISSN 1947-5500
2	Neural Based Scheduling Algorithm for Fixed Length Packet Switching	Mr. S.N.Kore Miss Poonam Bhosale	AES Journals in Engineering, Management & Sciences. Journal of information, knowledge and research in Electronics and communication engineering Vol 1, Issue 02, 2011 ISSN 0975-6779
3	Permuted Diagonal Maximum Weight Matching (PDMWM) Scheme for Cell Scheduling in Fixed Length Packet Switches	Sunil Kore, Ajinkya Biradar, Sayali Kore	IJCEM International Journal of Computational Engineering & Management, Vol. 15 Issue 4, July 2012 ISSN (Online): 2230-7893 www.IJCEM.org
4	A Performance Evaluation of Multiple Input Queued (MIQ) Switch with Iterative Weighted Slip Algorithm	S N Kore, Sayali Kore, Ajinkya Biradar Dr. P J Kulkarni	IJACSA International Journal of Advanced Computer Science and Applications, Vol. 3, No. 9, 2012 Pages 231-235, www.ijacsa.thesai.org
5	An Efficient Image Compression Technique using 2D-DWT and FELICS Algorithm for Different Class of Images (COI)	Sunil Kore , Akshay Bhosale	IJCEM International Journal of Computational Engineering & Management, Vol. 15 Issue 4, July 2012 pages 110- 114 ISSN (Online): 2230-7893 www.ijcem.org
6	Performance of Prioritized i-SLIP Algorithm with Multiple Input Queued Switch	S. N. Kore, Dr. P. J. Kulkarni	IJETAE International Journal of Emerging Technology and Advanced Engineering Website: www.ijetae.com ISSN 2250-2459, ISO 9001:2008 Certified Journal, Volume 5, Issue 8, August 2015, pages 267-274
7	Diagonalizable Algorithm for Cell Scheduling in Fixed Virtual Output Queued (VOQ) Packets Switches	Sunil N Kore; Dr.P.J.Kulkarni, Sayali Kore	IJIRCCE International Journal of Innovative Research in Computer and Communication Engineering (An ISO 3297: 2007 Certified Organization) Vol. 3, Issue 7, July 2015 pages 7037-7049. ISSN(Online): 2320-9801, ISSN (Print): 2320-9798

Note: List is not updated fully

Publications: International Conference and proceeding

Sr.No.	Title of the paper	Authors	Name of the Conference
1	Novel Solution for Ladder Diagram in Programmable logic controllers	S.N.Kore, N. V. Marathe	5 th International symposium on IC technology systems and application, Singapore ISIC 1993
2	"A new technique for Smoothing of MEPEG video traffic in ATM Networks"	V.B.Dharmadhkari S.N.Kore Dr. H.S. Jamdagani	5th Cananadian Conference for Broadband Research, Ottawa, CANADA. 1999
3	"Cell selection policies in multiple queued ATM switches"	S.N.Kore, Dr. H. S. Jamadagni	5th Cananadian Conference for Broadband Research, Ottawa, CANADA. 1999
4	"Robust Face Detection Using Adaboost Classifier"	Mr. S. N.Kore	Second International conference on "Advances in computer Vision and Information Technology" ACVIT 2009
5	IR Webcam Imaging and Vascular Pattern Analysis towards Hand Vein Authentication.	Mr. S. N. Kore Mr. Patil M. H. Mr. Rethrekar S.C Mr. Aditya Shrotri	2nd International Conf. on comp. and Automation Engg., Singapur , Feb 26-28 2010 Vol.5
6	Implementation of RISC Core on Reconfigurable Hardware	Mr. S. N. Kore Mr. A. S. Patil Miss S. R. Wadar Mr.D. R. Wadar	International Conf. on Emerging Trends in Engg. ICETE 2010
7	IR Webcam Imaging and Vascular Pattern Analysis towards Hand Vein Authentication.	Mr. S. N. Kore Mr. Patil M. H. Mr. Rethrekar S.C Mr. Aditya Shrotri	2nd International Conf. on comp. and Automation Engg., Singapur , Feb 20-21 Feb 2010 ICETE 2010 Pg. No. 31-35

Note: List is not updated fully

Publications: National Conference Proceedings

Sr.No.	Title of the paper	Authors	Name of the Conference
1	Prevention congestion control in ATM n/w using multi layer NN	S.N.Kore, H.S. Jamadagni	National conference on Comm. 1999
2	"Embedded system in automobiles " Best Paper Award	Mr. S. N. Kore, Mrs A. B. Patil, Miss Tandale	NCRVC national Conference and workshop on Virtual Instruments, Embedded systems and Robotics Gwailar, Mar 2004
3	Real Time Face detection using adaboost classifier	Mr. Waghmare H.K, Prof. S.N. Kore	National conference on "Emerging Trends in computer science and information technology" ETCSIT 17-18 April 2009
4	License Plate Recognition system	Mr. Prasenjeet D.Patil, Prof. S.N. Kore	National conference on "Emerging Trends in computer science and information technology" ETCSIT 17-18 April 2009
5	Review of Feature Extraction and Matching Techniques for CBIR	Mr. Kondekar V.H., Prof. S.N. Kore	National conference on Signal and Image processing Applications 19-20 Sept 2009
6	Face Detection Using Genetic Algorithm	Mr. S. N. Kore	National conference on "Application of computer Technology for rural Development in India" 3-4 July 2010

My Own Philosophy towards educational institute model and its dynamics

EDUCATIONAL INSTITUTIONAL MODEL AND ITS CHALLENGES, SOLUTIONS, METHODOLOGY

Mr. Sunil Kore

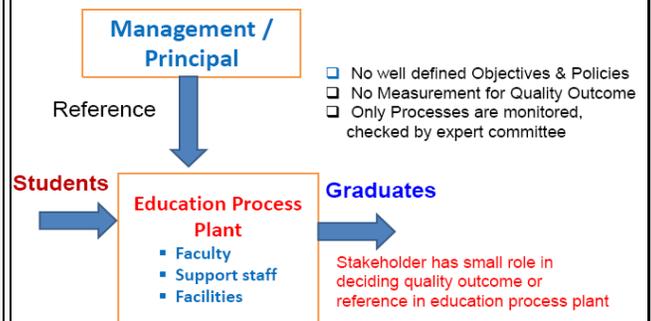
National - GOALS

- to improve overall quality of existing Engineering Education through, Enhancement of existing capacities of the institutions
- to become dynamic, demand-driven, quality conscious, efficient and forward looking, responsive to rapid economic and technological developments occurring at the local, State, National and International levels.

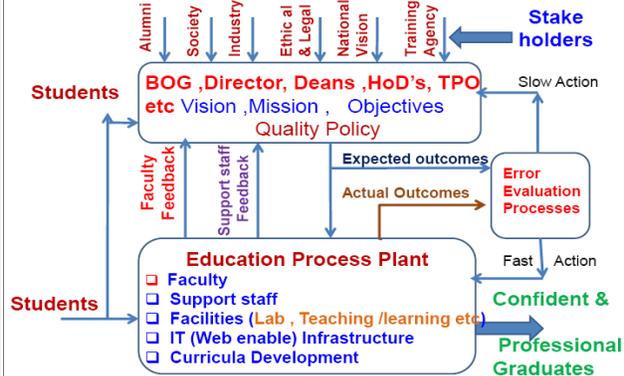
Nations- OBJECTIVES

- Strengthening institutions to produce high quality Engineers for better employability,
- **Scaling-up Postgraduate Education and demand - driven R & D Innovation,**
- Establishing Centers of Excellence for focused applicable research,
- **Training of faculty for effective Teaching,** (intends to improve learning outcomes , Employability of engineering students by improving competence of faculty, and Scale-up Research, Development and Innovation)
- **Enhancing Institutional and System Management effectiveness.**

OLD Institution Model



Multivariable Adaptive Institution



Understanding of Process Dynamics

Detect signals which may indicate a need to change.

Recognize a need for change and formulate a plan.

Make corporate decision to change and carry it through.

Challenges

- Mapping of expected outcome to the satisfaction of stakeholder
- Exact Measurements of outcomes
- Requirement of Huge and Complex Database Management System, Demands Web enabled IT infrastructure
- Responses of individual & Corrective Measures
- Implementation of Think – Pair – Share
- Creating Cooperative Environment
- Active Participation of Support Staff and their Training
- Finalizing area of Excellence
- Active ,Dynamic and Visionary leadership
- True Implementation of Government Resolutions/BOG Resolutions
- Identification of honest and dishonest stakeholders

- Students Feedback
- Faculty Feedback
- Support staff Feedback
- Alumni Feedback
- Industry Feedback
- Director's and HOD Specific Problems Issues and Opportunities (SPIO)

Possible Solutions

- Identification of Training Needs
- Micro level Planning in processes in administration, Usage of infrastructure & Human Resources and project management
- Revamping of Curriculum , Restructuring of academic calendar to satisfy interests of stake holders and obtain time for fulfilling objectives. STEP s needs to be Adopted

Actions Needed



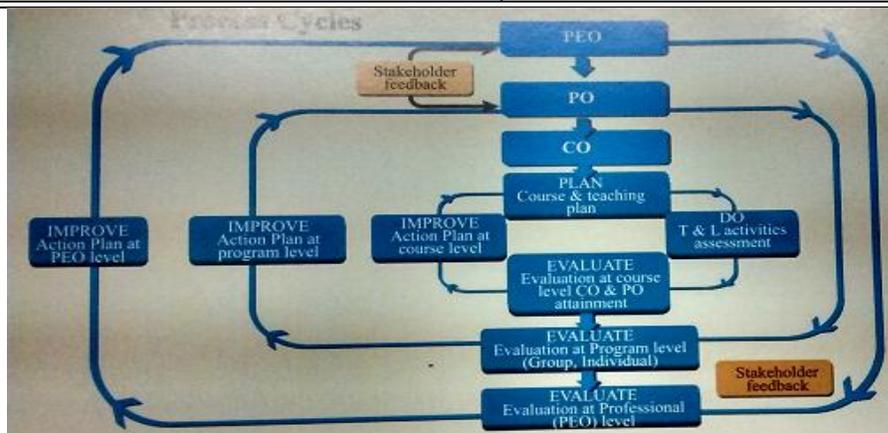
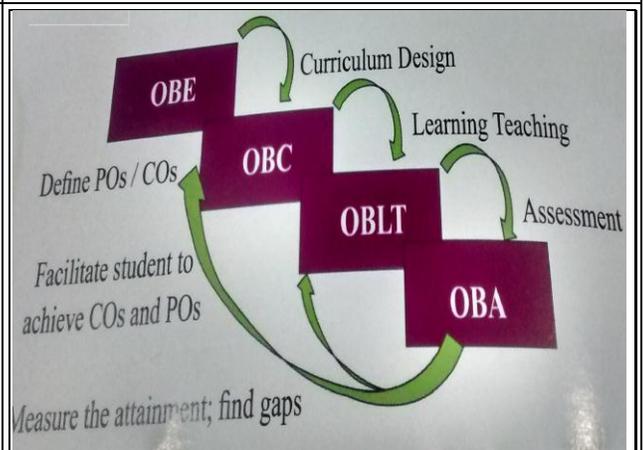
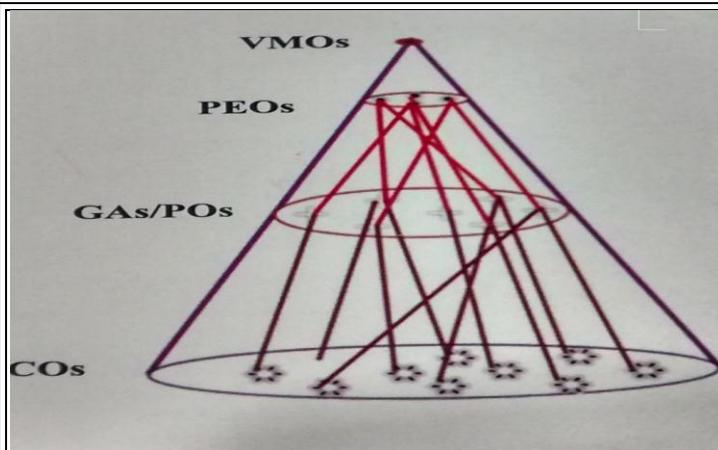
Review the system, update and reconstruct.

Search for new resources & use existing resources effectively.

Identify the truth, accept it , close- loop feedback system implementation.

Action Taken reports at all performance issues.

BOG and stake holders need to brain storm on the feedback, outcome and think of new strategic plan to obtain success in their Plan



Thanks to COE Karad for this model